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(Use as many sheets as necessary)

Complete if Known				
Application Number	10/729,970	_		
Filing Date	9 December 2003	_		
First Named Inventor	LIU, Yan-Fei	_		
Art Unit				
Examiner Name				
Attorney Docket Number	14546	フ		

	1 00		U. S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No.	Document Number Number-Kind Code ² (F known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
B		US- 2002/0118000 A1	08-29-2002	Xu et al	
AB		^{US-} 6,163,466	12-19-2000	Davila, Jr et al	
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		FORE	IGN PATENT DOCU	MENTS		
	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	Π
		Country Code ³ Number ⁴ 'Kind Code ⁵ (# known)	MM-DD-YYYY			74
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				Art Unit			
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Sheet	2	of	2	Attorney Docket Number	14546		

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Examiner Initials*	Cite No. ¹	NON PATENT LITERATURE DOCUMENTS Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
AS		PENG XU, FRED C LEE, JIA WEI; The Coupled-Buck Converter for 12V VRM; VTIP Disclosure No.: 01-002; Virginia Tech Intellectual Properties, Inc.; Blacksburg, Virginia	
B		ALBERT M. WU, SETH R. SANDERS; An Active Clamp Circuit for Voltage Regulation Module; IEEE Transactions On Power Electronics, Vol. 16 No. 5 September 2001 pp. 623-634	
B		YURI PANOV, MILAN M. JOVANOVIC; Design Considerations for 12-V/1.5V, 50A Voltage Regulator; IEEE Transactions On Power Electronics, Vol. 16 No. 6 November 2001 pp. 776-783	
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Examiner \ \(\lambda\) \(\frac{1}{2}\)	Date		,
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